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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO. 5341	
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22503	7590	05/17/2006		EXAMINER		
DAVIS &		ATES	SINGH, RAMNANDAN P			
P.O. BOX 1093 DRIPPING SPRINGS, TX 78620				ART UNIT	PAPER NUMBER	
				2614		
				DATE MAILED: 05/17/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application	Application No.		Applicant(s)	
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	Office Action Summary	Examine		Art Unit		
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Status	·					
1)⊠ 2a)□ 3)□	Responsive to communication(s) filed on 3 This action is FINAL . 2b) 2 Since this application is in condition for alloclosed in accordance with the practice und	This action is nowance except	on-final. for formal matters, pr		ne merits is	
Dispositi	on of Claims					
5)□ 6)⊠ 7)⊠ 8)□ Applicat i 9)□ 10)⊠	Claim(s) 1-18 is/are pending in the applicate 4a) Of the above claim(s) is/are with Claim(s) is/are allowed. Claim(s) 1-7,9-16 and 18 is/are rejected. Claim(s) 8 and 17 is/are objected to. Claim(s) are subject to restriction and an are subject to restriction and an are subject to restriction and an are subject to by the Example The drawing(s) filed on 31 December 2003. Applicant may not request that any objection to Replacement drawing sheet(s) including the control of the oath or declaration is objected to by the	drawn from cond/or election rainer. is/are: a)⊠ athe drawing(s) becomes	equirement. ccepted or b) object be held in abeyance. Se ed if the drawing(s) is o	ee 37 CFR 1.85(a). ojected to. See 37 0	CFR 1.121(d).	
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12) [a)[Acknowledgment is made of a claim for fore All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the papplication from the International But see the attached detailed Office action for a	nents have been pents have been periority documented (PCT Rul	n received. n received in Applica ents have been receive e 17.2(a)).	tion No red in this Nationa	al Stage	
2) 🔲 Notic 3) 🔲 Inforr	t(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB r No(s)/Mail Date		4) Interview Summar Paper No(s)/Mail D 5) Notice of Informal 6) Other:	ate	ГО-152)	

DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-7, 9-16, 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tanimoto et al [US 4,866,767] in view of Anderson [US 20040124996 A1].

Regarding claim 1, Tanimoto t al teach a subscriber line driver apparatus shown in Fig. 3, comprising:

an impedance bridge comprising a first impedance NZT (11), a second impedance MZB (12), and a capacitor C (28), and coupled to a subscriber line; and a first pair of current drivers (1, 2) coupled to the impedance bridge for driving a voice signal in a first frequency range (i.e. audio band) onto the subscriber line [Figs. 1-8; col. 4, line 45 to col. 6, line 12].

Tanimoto et al do not teach expressly using current drivers for driving a data signal in a second frequency range onto the subscriber line.

Anderson teaches using current drivers for driving a data signal in high frequency bands including ADSL and HDSL [Fig. 2; Para: 0003; 0099-0103; 0169-0198].

At the time of the invention, it would have been obvious to a person of ordinary skill in the art to combine the teachings of Anderson with Tanimoto et al in order to use a second pair of current drivers coupled to the impedance bridge to transmit data signals at high speeds [Anderson; Para: 0002-0003].

Further, the recitation Z1 + Z2 in claim # 1 is considered as an identification only, and does not constitute a limitation until they are defined in dependent claims.

Claim 10 is essentially similar to claim 1 and is rejected for the reasons stated above.

Regarding claim 4, Tanimoto et al further teach the subscriber line driver, wherein the first frequency range (i.e. voice frequency) has an upper bound of approximately 4 kHz [col. 4, lines 45-46].

Claim 13 is essentially similar to claim 4 and is rejected for the reasons stated above.

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Regarding claim 5, Anderson further teaches the apparatus, wherein the second frequency range (i.e. ADSL) includes a lower bound greater than 25 kHz [Para: 0003].

Claim 14 is essentially similar to claim 5 and is rejected for the reasons stated above.

Regarding claim 6, Anderson further teaches the subscriber line driver, wherein the ADSL includes discrete multi-tone encoded signals [Para: 0003].

Claim 15 is essentially similar to claim 6 and is rejected for the reasons stated above.

Regarding claim 7, Anderson further teaches the apparatus, wherein the impedance bridge comprises a first impedance NZT (11), a second impedance MZB (12), and a capacitor C (28) [Fig. 3].

Regarding claims 16 and 18, the limitations are shown above.

Regarding claim 9, Tanimoto et al further teach the apparatus, comprising:

an impedance synthesis circuit providing an impedance synthesis feedback
signal to the first pair of current drivers [Fig. 3], wherein within the first frequency range

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the output impedance across the subscriber line is controlled by the impedance synthesis circuit [col. 2, line 62 to col. 3, line 53; col. 5, line 34 to col. 6, line 22].

Regarding claim 2, although Tanimoto et al. teach a method for determining a value of NZT + MZB for a specific configuration of the impedance synthesis circuit [Figs. 3-4; col. 5, line 34 to col. 6, line 12; col. 9, line 34 to col. 12, line 59], they do not teach expressly the numerical value of the combined impedance ZT + ZB. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made, to determine the values of NZT + MZB of the impedance synthesis circuit for a specific configuration in order to accommodate the operation of the two current sources in the voice band subject to circuit, system and design constraints.

Claim 11 is essentially similar to claim 2 and is rejected for the reasons stated above.

Regarding claim 3, Tanimoto et al. teach the apparatus, wherein ZT approximately equals ZB for impedance balancing and reduced distortion [col. 9, lines 11-56].

Claim 12 is essentially similar to claim 3 and is rejected for the reasons stated above.

Allowable Subject Matter

3. Claims 8 and 17 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

Claim 8 identifies the uniquely distinct feature of a subscriber line driver apparatus, wherein a tip line of the subscriber line and one of the second pair of current drivers is connected to a first terminal of Z1, wherein a first terminal of C1 and one of the first pair of current drivers is connected to a second terminal of Z1, wherein the other of the second pair of current drivers is connected to a first terminal of Z2, wherein the other of the first pair of current drivers and a second terminal of C1 are connected to a second terminal of Z2. As such, claim 8 requires a unique configuration of the subscriber line driver as given here. While the closest prior art, Tanimoto et al [US 4,866,767] and Anderson [US 20040124996 A1] each teach providing configurations having current drivers, Tanimoto et al providing a configuration having current drivers for driving voice signals, and Anderson providing a configuration having current drivers for driving a data signal; neither Tanimoto et al nor Anderson show or suggest to provide the configuration having current drivers as claimed. As such, the prior art,

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either singularly or in combination, fail to anticipate or render the above underlined limitation obvious.

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Therefore, claim 8 is indicated allowable.

Claim 17 is essentially similar to claim 8 and hence claim 17 is also indicated allowable.

Conclusion

- 4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- (i) Breamer et al [US 6,782,096 B1] teach a subscriber line driver (SLD) for both POTS signals and DSL signals using current drivers [Figs. 2, 5A, 6; col. 4, lines 13-16; col. 4, line 31-34; col. 5, lines 18-25; col. 4, lines 43-48; col. 11, lines 15-27; col. 11, line 31 to col. 12, line 16];
- (ii) Bingel [US 5,999,619] teaches a method for a synthetic termination input impedance to a current driven data communication [Figs. 1-5; Abstract]; and
- (iii) Hanneberg et al [US 20040257114 A1] teach a line driver for data communication at high bit rates [Figs. 1-4; Abstract].
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ramnandan Singh whose telephone number is (571) 272-7529. The examiner can normally be reached on M-TH (8:00-5:30).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Fan Tsang can be reached on (571) 272-7547. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Ramnandan Singh

Examiner Art Unit 2614